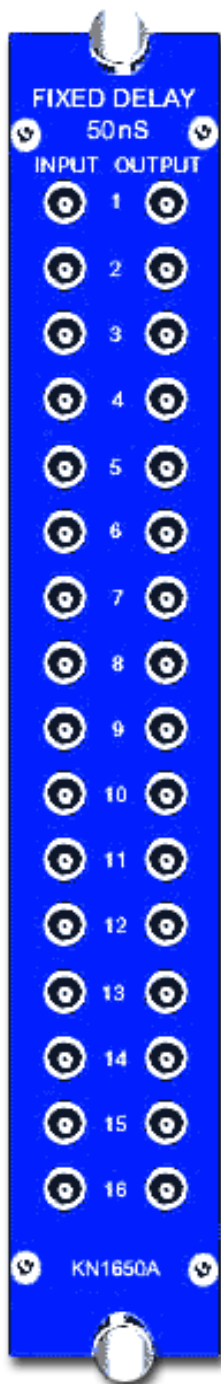


16ch FIXED LOGIC DELAY (50nS)



《ABSTRACT》

- ◆ This module has 16 channels time delay circuit made by logic circuit.
- ◆ The input and output signal is based on NIM standard of AEC.
(Atomic Energy Commission US)

《Specifications》

- ◇ Number of Input and output : 16
- ◇ Impedance : 50Ω
- ◇ Delay time : 50nS±1%
- ◇ Risetime,Falltime : Approx.1. 6nS
- ◇ Maximum frequency : 100MHz/10nS

- ◆ The input and output connector : Standard LEMO connector.

- ◆ Packaging : NIM standard single-width module.

- ◆ Consumption current -6V : Approx.1. 2A



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