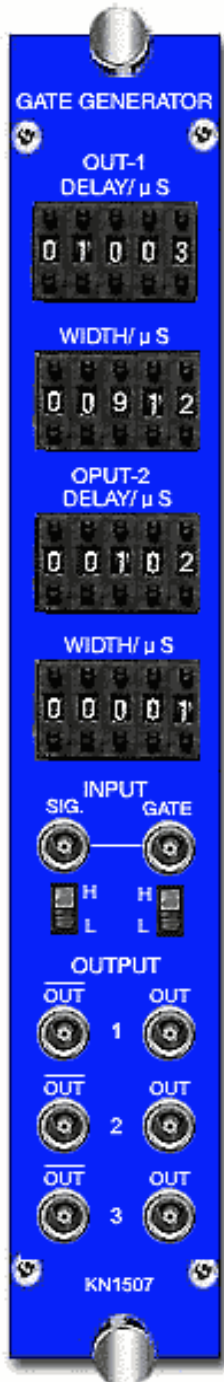


## GATE GENERATOR (RS422)



## 《ABSTRACT》

- ◆ This module outputs TTL pulse signal when input signal rises and falls.  
The output pulse can be specified the delay time and pulse width at will.
- ◆ This module has 6 outputs of normal 1,2,3 and inverse 1,2,3.  
Output1 corresponds to rise-timing of input signal. Output2 corresponds to fall-timing of input signal. Output3 outputs the logical OR signal of output 1 and 2, respectively.
- ◆ It can change the rise-timing and the fall-timing by the polarity change switch on front panel. (INPUT SIG and GATE)

## 《Specifications》

## INPUT characteristics ;

## SIG (input signal)

- ◇ TTL level signal
  - ◇ Impedance : 4.7 Ω
  - ◇ Period of input signal : 4 μS ~ ∞
  - ◇ Pulse width of input signal : 2 μS ~ ∞
  - ◇ Minimum load resistance : 50 Ω
  - ◇ Delay time : 0 ~ 99.999mS
  - ◇ Pulse width : 1 μS ~ 99.999mS
- GATE (Gate signal) TTL level signal

## OUTPUT characteristics ;

- ◇ OUT1 : Output(normal and inverse) synchronized rise-timing(fall-timing) of input signal.
- ◇ OUT2 : Output(normal and inverse) synchronized fall-timing(rise-timing) of input signal.
- ◇ OUT3 : Logical OR output(normal and inverse) of OUTPUT1 and 2.
- ◇ Reference time accuracy : ±100ppm Xtal controled
- ◇ Delay time error : Setting value ±200nS
- ◇ Pulse width error : Setting value ±200nS
- ◇ Jitter : 25nS max

- ◆ Delay time between input and output : 60 ± 10nS
- ◆ Input and output connector : Standard LEMO connector.
- ◆ Packaging : NIM standard single-width module.
- ◆ Consumption current -6V : Approx.100mA



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