

## 12ch 100MHz SCALER



### «ABSTRACT»

- ◆The input level is matched to NIM standard of AEC(Atomic Energy Commission US) and CAMAC high speed standard.
- ◆This module is embedded 12channel high speed 24bit counter and LAM bit. It can control each 12 counter to excute high speed inhibit and clear command on front panel at the same time.
- ◆This scaler circuit is used 2channel 11bit hybrid IC that is developed for exclusive use.
- ◆CAMAC function is as follows.
  - N•F(0) : Data Read.
  - N•F(1) : LAM bit Read.
  - N•F(2) :Data Read and Clear.
  - N•F(9) :Clear.
  - N•F(25) :Counter Increment.
- ◆Maximum frequency : 100MHz when input pulse width is 5ns.
- ◆The input connector : Standard LEMO connector
- ◆Packaging : CAMAC standard single-width module.
- ◆Consumption current : -6V Approx.1.6A  
+6V Approx.200mA



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