

8K WORD BUFFER MEMORY



《ABSTRACT》

- ◆ This module stores the data from ENCODE CONTROLLER of reading circuit for MWPC.
- ◆ The memory capacitor is 8k × 16bit.
- ◆ This module is used with ENCODE CONTROLLER KC3282.
- ◆ The number connected to ENCODE CONTROLLER is max. 16.
- ◆ If start signal is input to event start terminal of buffer memory module, it starts data transfer between this module and encode controller. At this time, it is saved the event number in the first address automatically. If it receives the encode end signal from encode controller, the data transfer is finished.

《Specifications》

- ◆ CAMAC function is as follows.
 - F(0)•A(0) : Read LAM Data and Increment RAM Address Counter
 - F(1)•A(0) : Read RAM Address Counter
 - F(1)•A(1) : Read Event Counter
 - F(8)•A(0) : Test FT Stop LAM
 - F(8)•A(1) : Test RAM Address Counter Over Flow LAM
 - F(9)•A(0) : System Clear
 - F(10)•A(0) : Clear FT Stop LAM
 - F(10)•A(1) : Clear RAM Address Counter Over Flow LAM
 - F(16)•A(0) : Write RAM Data and Increment RAM Address Counter
 - F(17)•A(0) : Write RAM Address Counter
 - F(24)•A(0) : Disable LAM
 - F(26)•A(0) : Enable LAM
- ◆ Packaging : CAMAC standard single-width module.
- ◆ Consumption current : -6V Approx.60mA
+6V Approx.1A



KAIZUWORKS CORPORATION
 9-18 HIGASHIMATSUBARA HAKONEGASAKI MIZUHOMACHI
 NISHITAMAGUN TOKYO 190-1222 JAPAN
 Tel : 042-568-0866 Fax : 042-568-0867

E-mail kaizuwks@kaizuworks.co.jp Homepage <http://www.kaizuworks.co.jp>